

Claims

What is claimed is:

- 1 1. A system comprising:  
2 a computer system including a processor and a memory and  
3 configured to boot using a system firmware;  
4 the system firmware including instructions for causing the computer  
5 system to:  
6 detect a test apparatus coupled to the computer system; and  
7 initiate a manufacturing mode of the system firmware in  
8 response to detecting the test apparatus coupled to the computer system.
- 1 2. The system of claim 1, wherein the system firmware includes instructions for  
2 causing the computer system to:  
3 provide a first value to the test apparatus;  
4 receive a second value from the test apparatus in response to  
5 providing the first value to the test apparatus; and  
6 initiate the manufacturing mode in response to receiving the second  
7 value from the test apparatus.
- 1 3. The system of claim 2, wherein the system firmware includes instructions for  
2 causing the computer system to:  
3 store the first value in a first storage location;  
4 store a third value in a second storage location; and  
5 receive the second value from a third storage location identified by the  
6 third value.

- 1    4.    The system of claim 3, wherein the system firmware includes instructions for  
2       causing the computer system to:  
3                 perform an operation to cause the test apparatus to receive the first  
4       value and the third value.
- 1    5.    The system of claim 4, wherein the operation is an input / output operation.
- 1    6.    The system of claim 4, wherein the operation is a memory operation.
- 1    7.    The system of claim 2, wherein the system firmware includes instructions for  
2       causing the computer system to:  
3                 not initiate the manufacturing mode in response to not receiving the  
4       second value from the test apparatus.
- 1    8.    The system of claim 1, wherein the manufacturing mode of the system  
2       firmware includes instructions for causing the computer system to:  
3                 receive information from the test apparatus; and  
4                 store the information on a device in the computer system.
- 1    9.    The system of claim 1, wherein the manufacturing mode of the system  
2       firmware includes instructions for causing the computer system to:  
3                 store the system firmware on a device in the computer system.

1    10.    A computer program product comprising:  
 2                    a system firmware processable by a computer system for causing the  
 3    computer system to:  
 4                    detect a test apparatus coupled to the computer system; and  
 5                    initiate a manufacturing mode of the system firmware in  
 6    response to detecting the test apparatus coupled to the computer system;  
 7    and  
 8                    a storage apparatus from which the system firmware is accessible by  
 9    the computer system.

1    11.    The computer program product of claim 10, the system firmware processable  
 2    by the computer system for causing the computer system to:  
 3                    provide a first value to the test apparatus;  
 4                    receive a second value from the test apparatus in response to  
 5    providing the first value to the test apparatus; and  
 6                    initiate the manufacturing mode in response to receiving the second  
 7    value from the test apparatus.

1    12.    The computer program product of claim 11, the system firmware processable  
 2    by the computer system for causing the computer system to:  
 3                    store the first value in a first storage location;  
 4                    store a third value in a second storage location; and  
 5                    receive the second value from a third storage location identified by the  
 6    third value.

1    13.    The computer program product of claim 12, the system firmware processable  
 2    by the computer system for causing the computer system to:  
 3                    perform an operation to cause the test apparatus to receive the first  
 4    value and the third value.

- 1 14. The computer program product of claim 13, wherein the operation is an input  
2 / output operation.
- 1 15. The computer program product of claim 13, wherein the operation is a  
2 memory operation.
- 1 16. The computer program product of claim 11, the system firmware processable  
2 by the computer system for causing the computer system to:  
3 not initiate the manufacturing mode in response to not receiving the  
4 second value from the test apparatus.
- 1 17. The computer program product of claim 10, the manufacturing mode of the  
2 system firmware processable by the computer system for causing the  
3 computer system to:  
4 receive information from the test apparatus; and  
5 store the information on a device in the computer system.
- 1 18. The computer program product of claim 10, the manufacturing mode of the  
2 system firmware processable by the computer system for causing the  
3 computer system to:  
4 store the system firmware on a device in the computer system.
- 1 19. A method performed by a computer system comprising:  
2 booting the computer system using a system firmware;  
3 detecting a test apparatus coupled to the computer system; and  
4 initiating a manufacturing mode of the system firmware in response to  
5 detecting the test apparatus coupled to the computer system.

- 1    20.    The method of claim 19, further comprising:  
2                    providing a first value to the test apparatus;  
3                    receiving a second value from the test apparatus in response to  
4                    providing the first value to the test apparatus; and  
5                    initiating the manufacturing mode in response to receiving the second  
6                    value from the test apparatus.
- 1    21.    The method of claim 20, further comprising:  
2                    storing the first value in a first storage location;  
3                    storing a third value in a second storage location; and  
4                    receiving the second value from a third storage location identified by  
5                    the third value.
- 1    22.    The method of claim 21, further comprising:  
2                    performing an operation to cause the test apparatus to receive the first  
3                    value and the third value.
- 1    23.    The method of claim 22, further comprising:  
2                    performing the operation to cause the test apparatus to receive the  
3                    first value and the third value, wherein the operation is an input / output  
4                    operation.
- 1    24.    The method of claim 22, further comprising:  
2                    performing the operation to cause the test apparatus to receive the  
3                    first value and the third value, wherein the operation is a memory operation.

- 1    25.    The method of claim 20, further comprising:  
2                    not initiating the manufacturing mode in response to not receiving the  
3                    second value from the test apparatus.
- 1    26.    The method of claim 19, further comprising:  
2                    in response to initiating the manufacturing mode of the system  
3                    firmware:  
4                    receiving information from the test apparatus; and  
5                    storing the information on a device in the computer system.
- 1    27.    The method of claim 19, further comprising:  
2                    in response to initiating the manufacturing mode of the system  
3                    firmware:  
4                    storing the system firmware on a device in the computer  
5                    system.
- 1    28.    A system comprising:  
2                    a test apparatus;  
3                    a circuit including a system firmware; and  
4                    a computer system coupled to the circuit, configured to boot using the  
5                    system firmware and configured to provide a first signal to the test apparatus;  
6                    the test apparatus configured to provide a second signal to the  
7                    computer system in response to receiving the first signal; and  
8                    the computer system configured to initiate a manufacturing mode of  
9                    the system firmware in response to receiving the second signal.

- 1    29.    The system of claim 28, wherein the computer system is configured to store  
2           the first signal as a first value in a first storage location, and wherein the test  
3           apparatus is configured to store the second signal as a second value in a  
4           second storage location.
  
- 1    30.    The system of claim 29, wherein the computer system is configured to store a  
2           third value in a third storage location, wherein the test apparatus is configured  
3           to receive the third value, and wherein the third value identifies the second  
4           storage location.